



Zn₂SiO₄ and SnO₂ nanowires synthesized by thermal ramping technique

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ABSTRACT

In this report, nanowires of Zn₂SiO₄ and SnO₂ have been successfully synthesized using a simple novel method namely thermal ramping technique. The technique applies thermal ramping of the sample to a temperature of 900 °C using carbothermal reduction method. This technique requires no carrier gas. Elemental analysis and crystal structure were evaluated using Field Emission Scanning Electron Microscopy (FESEM) , Energy Dispersive X-ray (EDX) and X-ray Diffraction(XRD) analysis. Nanowires of SnO₂ and Zn₂SiO₄ with diameter ranging from 15-25 nm were observed. The effect of Au thickness in synthesizing Zn₂SiO₄ is also discussed.

Key words: Thermal Ramping, Carbothermal Technique, SnO₂ Nanowires

INTRODUCTION

Nanowires has gained a lot of attention in various applications (Thong, Loan, and Hien 2010, Zang et al, 2011, Peng et al, 2009) due to its unique physical and electronic structural. Since then, various method have been studied and developed in synthesizing nanowires (Duraja, Mansorov, and Tokmolden 2009, Korotcenkov et al, 2010, Chen et al, 2009). Carbothermal technique is one of the simplest techniques used to synthesize nanowires. Nanowires of SnO₂ for example, have been synthesized using this method and used in numerous gas sensing research. Reports have proved its suitability for gas sensing applications. However there could be more research in enhancing both the selectivity and sensitivity of the sensor. Apart from SnO₂, there have been several researches (Xu et al, 2003, Li et al, 2010, An et al, 2010) on Zn₂SiO₄ and its possible role in future nanotechnology. It is said that its magnitude of absorption coefficient, reflectivity, refractive index and extinction coefficient are suitable for antireflection coating applications (Karazhanov et al, 2009). In this report the synthesis technique is based on the modified carbothermal technique. Carbothermal technique is chosen due to its capability to vaporize a material at lower temperature. Gold

catalyst used induces the nanowires growth by vapor-liquid-solid (VLS) mechanism. The introduction of ramping elements to the conventional technique has successfully emulated the suitable and required atmosphere for the growth of the nanowires. The new technique developed has proven to be simple, reliable and cheap, without incorporating too many parameters such as pressure of the deposition chamber, source to substrate distance and the flow rate of the carrier gas, which are indispensable in the conventional carbothermal technique

EXPERIMENTAL PROCEDURE

Silicon substrates were cleaned in ethanol and treated with ultrasonic bath, etched in 65 vol. % HNO₃ aqueous solution prior to the experiment. Then the substrates were coated with Au, 10 nm and 25 nm in thickness respectively. ZnO powder (99.5% purity) and carbon powder (99.5 % purity) with molar ratio of 1:2 were used as the source material and thoroughly mixed. The source material and substrate were later loaded into the same porcelain boat and placed in the middle of an alumina tube. The tube is 2.0 inches of external diameter, 1.85 inches of internal diameter and 24 inches length. The alumina tube was

put inside the furnace horizontally. The sample was heated according to Fig. 1 ramping pattern.

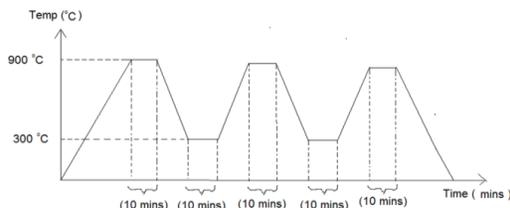


Figure 1. Ramping pattern of the temperature applied during the heating process

As for synthesizing SnO₂ nanowires, the whole procedures were then repeated using SnO₂ as the precursor material. Electron Microscopy (FESEM), Energy Dispersive X-ray (EDX) and X-ray Diffraction (XRD) were used to analyse the morphology, elemental component and crystal structure of the nanostructures.

Zn₂SiO₄ NANOWIRES

Figure 2 shows the FESEM images of the Zn₂SiO₄ nanowires. From the images, the length can be approximated to be 2 – 3 μm whereas the diameter ranges from 15 – 25 nm (refer Fig. 2c). The growth of the Zn₂SiO₄ nanowires does not follow any proper pattern. However, there is significant effect to the density of the nanowires when using different thickness of gold catalyst (refer to Fig 2a and 2b). The increase in the gold thickness results in low density of Zn₂SiO₄ nanowires.

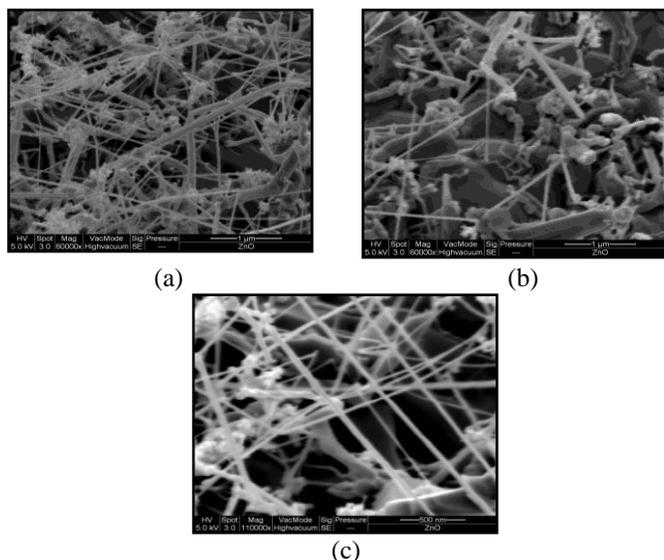


Figure 2. FESEM images of Zn₂SiO₄ nanowires prepared with different Au coating thickness: (a) 10nm (b) 25 nm and (c) 10 nm (different magnification)

XRD spectrum of the nanowires is indicated in Fig. 3 with its strong peaks are labelled. XRD results indicate that these peaks can be indexed to Zn₂SiO₄ compound. The lattice constants are

$a = b = 13.948 \text{ \AA}$ and $c = 9.315 \text{ \AA}$ which corresponds to rhombohedral structure.

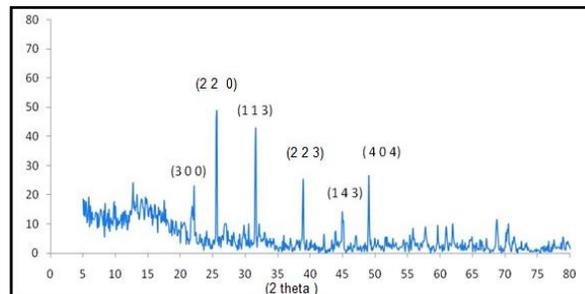


Figure 3. XRD pattern of synthesized Zn₂SiO₄ nanowires

In this experiment, carbon was used as a reduction agent to lower the vaporizing temperature of ZnO. Au catalyst was used to induce the growth of the nanowires by Vapor-Liquid-Solid (VLS) mechanism.



The formation of Zn₂SiO₄ can be explained as follow. As the temperature is ramped up to 900 °C, ZnO decomposes to Zn and CO. Vapors of Zn fills inside the alumina tube. Some of the vapor will escape through the cap at both end of the tube. Instead of using carrier gas to transport these vapors to the substrate (substrates and ZnO powder are placed in the same porcelain boat), the temperature is ramped down or cooled down to force the vapors to lose its kinetic energy. The vapors will then get condensed on the Au-coated substrate before being oxidised back to ZnO by the O in the surrounding. These ZnO droplets are then react with Si-O from the substrate(oxidised Si melted surface) and together with Au they form ZnO/SiO₂/Au alloy. The process of forming ZnO/SiO₂/Au alloy recurs as the temperature is ramped up and down for the second and third time. Due to the high temperature during the ramp up process, the alloy will quickly combine to form Zn₂SiO₄ (Wang, H.Q et al, 2007). There is another possibility that instead of ZnO/SiO₂/Au alloy formation, Zn/Si/Au alloy is formed. This alloy droplets will then get oxidised by the O in the surrounding to directly form Zn₂SiO₄ (Wang, X., Summers, C.J. and Wang, Z.L., 2004).



We assume the Au has been left out without forming any bond with Zn or Si since there is no Au-Zn or Au-Si intermetallic compound detected by XRD analysis. Continuous supply of Zn vapors results in the supersaturation of the alloy and thus crystallisation takes place. The crystallisation then acts as the nucleation sites and initiates the growth of the Zn₂SiO₄ nanowires. The thickness of Au-catalyst could affect the nanowires density by indirect controlling the supersaturating level of the ZnO/SiO₂/Si or Zn/Si/Au alloy. Since the nanowires starts to grow only when the alloy supersaturates,

thus the thinner the Au layer, the faster the alloy droplets will get supersaturated. This way, the growth of the nanowires can be initiated faster. Although no detail studies of Au-catalyst role in the formation of Zn_2SiO_4 nanowires is carried out, we believe that it is sufficient to roughly procure the idea of how the Au layer plays its role.

SnO₂ NANOWIRES

FESEM images of the SnO₂ nanowires grown on silicon substrate are shown in Fig. 4. As in the case of SnO₂ nanowires, Fig. 4(a) shows that the nanowires formed also do not follow any proper alignment or pattern. The diameter of the nanowires ranges from 15-25 nm and the length extends to few micrometers.

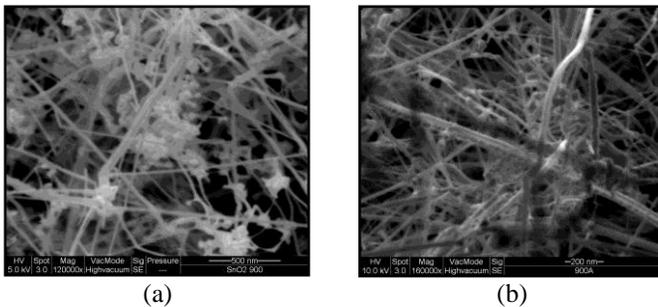


Figure 4. SnO₂ nanowires at different magnification and different spot. (a) 120 000 x and (b) 160 000 x

We used energy dispersed x-ray spectroscopy (EDX) to analyse the elemental components of the nanowires. Fig. 5 shows the elemental components of the SnO₂ nanowires. The result shows that the nanowires composed of Sn, O, Si and Au elements with weight percentage 36.99 %, 46.67 %, 15.66 % and 0.68 % respectively. The present of Si and Au elements come from the Si substrate and gold catalyst used respectively. The XRD results indicate several peaks correspond to SnO₂ crystal planes which can be indexed to tetragonal rutile structure of SnO₂ with lattice constant $a = b = 4.738 \text{ \AA}$ and $c = 3.187 \text{ \AA}$.

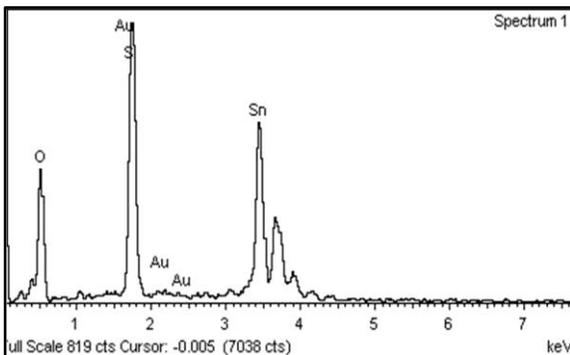


Figure 5. EDX profile of SnO₂ nanowires

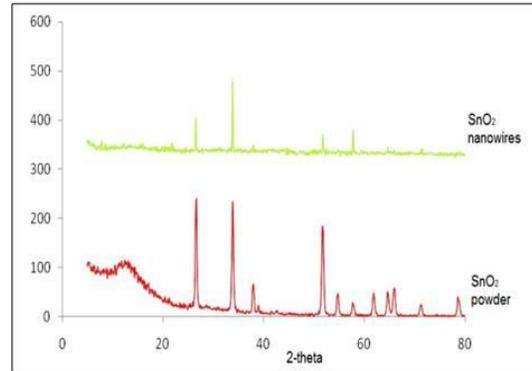
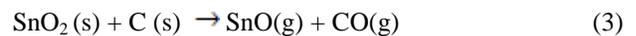


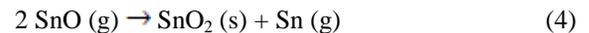
Figure 6. XRD pattern of the synthesized nanowires

The presence of the droplets at the nanowires (observe from the FESEM images) suggests that the growth of the SnO₂ nanowires follows general vapour-liquid-solid (VLS) mechanism (Dayeh, S.A., Yu, E.T. and Wang, D., 2007).

In carbothermal reaction, the SnO₂ is first reduced by carbon according to the following chemical reaction (Wang, B. et al, 2009):



Since the vapour of SnO(g) produced is metastable, it will decompose to Sn and SnO₂ when the temperature is ramped down (Messing et al, 2009) :



Thus, the formation of the nanowires can be explained by the reaction of the vapour with the Au catalyst on the surface of the substrate. At high temperature, Au thin film forms liquid spherical particles on the Si substrate. The particles will then act as adsorption sites for the Sn vapour to deposit. This creates Au and Sn mixture or Au/Sn alloy particle (Salehi, Janfeshan and Sadmezhaad, 2009).

Later, these alloys provide energetically preferred sites for the adsorption of SnO vapour (Wang, J.X. et al, 2004). The vapour at the surface of alloy then reacts with traces of O₂ in surrounding to form SnO₂ before diffuses into the alloy droplets. The continuous deposition and diffusion (through repetition of ramping up and down) make the alloy supersaturated and crystallisation of SnO₂ will then create solid/liquid interfaces of alloy and SnO₂ crystal. The nucleation starts and the SnO₂ growth occur.

CONCLUSION

In this experiment, the SnO₂ and Zn₂SiO₄ nanowires with diameter ranging from 15-25 nm have been successfully synthesized by thermal ramping technique. The nanowires however do not follow any growth pattern. Both nanowires formation are governed by VLS mechanism which is induced by the usage of the Au catalyst. Apart from catalyst role, the different in the Au thickness does also contribute in the densification of the nanowires. Lowering the thickness of Au

has resulted in the denser nanowires and vice versa. The thermal ramping technique has proven to successfully create the same reaction atmosphere as other techniques do to synthesize nanowires. The technique omits several parameters which are usually indispensable in the conventional

carbothermal technique such as material to substrate distance, carrier gas and its flow rate. Incorporating the ramping in temperature has proven to be a successful substitute for the omitted parameters and thus simplified the process.

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